

REMARKS

In the Office Action of December 23, 2005, the Examiner rejected claim 1 as anticipated by Patel (U.S. Pat. No. 6,826,749). Patel shows in Figures 1 and 3 a CPU 25 that executes non-Java bytecodes. To the extent, Java bytecodes are present in the instruction cache 24, such bytecodes are converted to one or more non-Java instructions by the Java accelerator instruction translation 42. A multiplexer 28 is included to receive the converted non-Java instructions from the translation unit 42 or non-Java instructions directly from the instruction cache 24 and, in either case, provides non-Java instructions to the CPU's instruction fetch 26a.

Applicants amend claim 1 to require the processor to include "an instruction fetch logic that receives at least stack-based instructions from the stack-based instruction set." The instruction fetch logic of Patel does not receive stack-based instructions (Java bytecodes). The purpose of the translation unit 42 is to convert Java bytecodes into the native instructions of the CPU. For at least this reason, claim 1 and all claims dependent thereon are in condition for allowance.

An amendment similar to that for claim 1 has been to claims 8 and 30. Hендлер (U.S. Pat. No. 6,473,777) was also combined with Patel in an obviousness rejection of claim 30. Hендлер does not satisfy the deficiency of Patel in light of the amendment to claim 30. Thus, claims 8 and 30 and their dependent claims are in condition for allowance.

Applicants include the limitations from dependent claim 17 into independent claim 12. Claim 17 was rejected as obvious over, in part, Balmer (U.S. Pat. No. 6,839,831). Balmer, however, qualifies as prior art only under 35 U.S.C. § 102(e) and is owned by the same entity as the present application. Per § 103(c), Balmer, being commonly-owned §102(e) art, cannot be used as prior art in an obviousness rejection in this case. No other art of record satisfies the void left by Balmer. For at least this reason, claim 12 and its dependent claims are in condition for allowance.

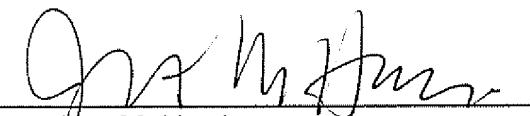
Claims 28-29 have been canceled thereby mooting the rejection of such claims.

**Appl. No. 10/631,308
Amdt. dated June 23, 2006
Reply to Office Action of December 23, 2005**

Applicants also provide an amendment paragraph [0001] to include the serial numbers for the cross-referenced list of applications.

Applicants hereby request a three-month time extension and authorize the Office to charge Texas Instruments Inc.'s Deposit Account No. 20-0668 for such fees. Applicants respectfully request reconsideration and that a timely Notice of Allowance be issued in this case. In the event that additional fees related to this Amendment, or other transactions in this case, are required (including fees for net addition of claims), Examiner is authorized to charge Texas Instruments Inc.'s Deposit Account No. 20-0668 for such fees.

Respectfully submitted,



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